

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
B.Tech. in ELECTRONICS AND COMMUNICATION ENGINEERING

III YEAR LABS SYLLABUS (R18)
Applicable From 2018-19 Admitted Batch

MICROPROCESSORS AND MICROCONTROLLERS LAB

B.Tech. III Year I Semester

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Cycle 1: Using 8086 Processor Kits and/or Assembler (5 Weeks)

- Assembly Language Programs to 8086 to Perform
 1. Arithmetic, Logical, String Operations on 16 Bit and 32-Bit Data.
 2. Bit level Logical Operations, Rotate, Shift, Swap and Branch Operations.

Cycle 2: Using 8051 Microcontroller Kit (6 weeks)

- Introduction to IDE
 1. Assembly Language Programs to Perform Arithmetic (Both Signed and Unsigned) 16 Bit Data Operations, Logical Operations (Byte and Bit Level Operations), Rotate, Shift, Swap and Branch Instructions
 2. Time delay Generation Using Timers of 8051.
 3. Serial Communication from / to 8051 to / from I/O devices.
 4. Program Using Interrupts to Generate Square Wave 10 KHZ Frequency on P2.1 Using Timer 0 8051 in 8 bit Auto reload Mode and Connect a 1 HZ Pulse to INT1 pin and Display on Port 0. Assume Crystal Frequency as 11.0592 MHZ

Cycle 3: Interfacing I/O Devices to 8051(5 Weeks)

1. 7 Segment Display to 8051.
2. Matrix Keypad to 8051.
3. Sequence Generator Using Serial Interface in 8051.
4. 8 bit ADC Interface to 8051.
5. Triangular Wave Generator through DAC interfaces to 8051.

TEXT BOOKS:

1. Advanced Microprocessors and Peripherals by A K Ray, Tata McGraw-Hill Education, 2006
2. The 8051 *Microcontrollers*: Architecture, Programming & Applications by Dr. K. Uma Rao, Andhe Pallavi, Pearson, 2009.

DATA COMMUNICATIONS AND NETWORKS LAB

B.Tech. III Year I Semester

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Note:

- A. Minimum of 12 Experiments have to be conducted
- B. All the Experiments may be Conducted using Network Simulation software like NS-2, NSG-2.1 and Wire SHARK/equivalent software.

Note: For Experiments 2 to 10 Performance may be evaluated through simulation by using the parameters Throughput, Packet Delivery Ratio, Delay etc.

1. Writing a TCL Script to create two nodes and links between nodes
2. Writing a TCL Script to transmit data between nodes
3. Evaluate the performance of various LAN Topologies
4. Evaluate the performance of Drop Tail and RED queue management schemes
5. Evaluate the performance of CBQ and FQ Scheduling Mechanisms
6. Evaluate the performance of TCP and UDP Protocols
7. Evaluate the performance of TCP, New Reno and Vegas
8. Evaluate the performance of AODV and DSR routing protocols
9. Evaluate the performance of AODV and DSDV routing protocols
10. Evaluate the performance of IEEE 802.11 and IEEE 802.15.4
11. Evaluate the performance of IEEE 802.11 and SMAC
12. Capturing and Analysis of TCP and IP Packets
13. Simulation and Analysis of ICMP and IGMP Packets
14. Analyze the Protocols SCTP, ARP, NetBIOS, IPX VINES
15. Analysis of HTTP, DNS and DHCP Protocols

Major Equipment Required:

Required software (Open Source) like NS-2, NSG-2.1 and Wire SHARK

ADVANCED COMMUNICATION SKILLS LAB

B.Tech. III Year I Semester

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1. INTRODUCTION:

The introduction of the Advanced Communication Skills Lab is considered essential at 3rd year level. At this stage, the students need to prepare themselves for their careers which may require them to listen to, read, speak and write in English both for their professional and interpersonal communication in the globalized context.

The proposed course should be a laboratory course to enable students to use 'good' English and perform the following:

- Gathering ideas and information to organize ideas relevantly and coherently.
- Engaging in debates.
- Participating in group discussions.
- Facing interviews.
- Writing project/research reports/technical reports.
- Making oral presentations.
- Writing formal letters.
- Transferring information from non-verbal to verbal texts and vice-versa.
- Taking part in social and professional communication.

2. OBJECTIVES:

This Lab focuses on using multi-media instruction for language development to meet the following targets:

- To improve the students' fluency in English, through a well-developed vocabulary and enable them to listen to English spoken at normal conversational speed by educated English speakers and respond appropriately in different socio-cultural and professional contexts.
- Further, they would be required to communicate their ideas relevantly and coherently in writing.
- To prepare all the students for their placements.

3. SYLLABUS:

The following course content to conduct the activities is prescribed for the Advanced English Communication Skills (AECS) Lab:

1. **Activities on Fundamentals of Inter-personal Communication and Building Vocabulary** - Starting a conversation – responding appropriately and relevantly – using the right body language – Role Play in different situations & Discourse Skills- using visuals - Synonyms and antonyms, word roots, one-word substitutes, prefixes and suffixes, study of word origin, business vocabulary, analogy, idioms and phrases, collocations & usage of vocabulary.
2. **Activities on Reading Comprehension** –General Vs Local comprehension, reading for facts, guessing meanings from context, scanning, skimming, inferring meaning, critical reading& effective googling.
3. **Activities on Writing Skills** – Structure and presentation of different types of writing – *letter writing/Resume writing/ e-correspondence/Technical report writing/* – planning for writing – improving one's writing.
4. **Activities on Presentation Skills** – Oral presentations (individual and group) through JAM sessions/seminars/PPTs and written presentations through posters/projects/reports/ e-mails/assignments etc.
5. **Activities on Group Discussion and Interview Skills** – Dynamics of group discussion, intervention, summarizing, modulation of voice, body language, relevance, fluency and organization of ideas and rubrics for evaluation- Concept and process, pre-interview planning, opening strategies, answering strategies, interview through tele-conference & video-conference and Mock Interviews.

4. MINIMUM REQUIREMENT:

The Advanced English Communication Skills (AECS) Laboratory shall have the following infrastructural facilities to accommodate at least 35 students in the lab:

- Spacious room with appropriate acoustics.
- Round Tables with movable chairs
- Audio-visual aids
- LCD Projector
- Public Address system
- P – IV Processor, Hard Disk – 80 GB, RAM–512 MB Minimum, Speed – 2.8 GHZ
- T. V, a digital stereo & Camcorder
- Headphones of High quality

5. SUGGESTED SOFTWARE:

The software consisting of the prescribed topics elaborated above should be procured and used.

- Oxford Advanced Learner's Compass, 7th Edition
- DELTA's key to the Next Generation TOEFL Test: Advanced Skill Practice.
- Lingua TOEFL CBT Insider, by Dream tech
- TOEFL & GRE (KAPLAN, AARCO & BARRONS, USA, Cracking GRE by CLIFFS)

TEXT BOOKS:

1. Effective Technical Communication by M Asharaf Rizvi. McGraw Hill Education (India) Pvt. Ltd. 2nd Edition
2. Academic Writing: A Handbook for International Students by Stephen Bailey, Routledge, 5th Edition.

REFERENCES:

1. Learn Correct English – A Book of Grammar, Usage and Composition by Shiv K. Kumar and Hemalatha Nagarajan. Pearson 2007
2. Professional Communication by Aruna Koneru, McGraw Hill Education (India) Pvt. Ltd, 2016.
3. Technical Communication by Meenakshi Raman & Sangeeta Sharma, Oxford University Press 2009.
4. Technical Communication by Paul V. Anderson. 2007. Cengage Learning pvt. Ltd. New Delhi.
5. English Vocabulary in Use series, Cambridge University Press 2008.
6. Handbook for Technical Communication by David A. McMurrey & Joanne Buckley. 2012. Cengage Learning.
7. Communication Skills by Leena Sen, PHI Learning Pvt Ltd., New Delhi, 2009.
8. Job Hunting by Colm Downes, Cambridge University Press 2008.
9. English for Technical Communication for Engineering Students, Aysha Vishwamohan, Tata Mc Graw-Hill 2009.

DIGITAL SIGNAL PROCESSING LAB

B.Tech. III Year II Semester

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The Programs shall be implemented in Software (Using MATLAB / Lab View / C Programming/ Equivalent) and Hardware (Using TI / Analog Devices / Motorola / Equivalent DSP processors).

Note: - Minimum of 12 experiments has to be conducted.

List of Experiments:

1. Generation of Sinusoidal Waveform / Signal based on Recursive Difference Equations
2. Histogram of White Gaussian Noise and Uniformly Distributed Noise.
3. To find DFT / IDFT of given DT Signal
4. To find Frequency Response of a given System given in Transfer Function/ Differential equation form.
5. Obtain Fourier series coefficients by formula and using FET and compare for half sine wave.
6. Implementation of FFT of given Sequence
7. Determination of Power Spectrum of a given Signal(s).
8. Implementation of LP FIR Filter for a given Sequence/Signal.
9. Implementation of HP IIR Filter for a given Sequence/Signal
10. Generation of Narrow Band Signal through Filtering
11. Generation of DTMF Signals
12. Implementation of Decimation Process
13. Implementation of Interpolation Process
14. Implementation of I/D Sampling Rate Converters
15. Impulse Response of First order and Second Order Systems.

e - CAD LAB

B.Tech. III Year II Semester

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Note: Any **SIX** of the following experiments from each part are to be conducted (Total 12)

Part - I

All the following experiments have to be implemented using HDL

1. Realize all the logic gates
2. Design of 8-to-3 encoder (without and with priority) and 2-to-4 decoder
3. Design of 8-to-1 multiplexer and 1-to-8 demultiplexer
4. Design of 4 bit binary to gray code converter
5. Design of 4 bit comparator
6. Design of Full adder using 3 modeling styles
7. Design of flip flops: SR, D, JK, T
8. Design of 4-bit binary, BCD counters (synchronous/ asynchronous reset) or any sequence counter
9. Finite State Machine Design

Part-II

Layout, physical verification, placement & route for complex design, static timing analysis, IR drop analysis and crosstalk analysis for the following:

1. Basic logic gates
2. CMOS inverter
3. CMOS NOR/ NAND gates
4. CMOS XOR and MUX gates
5. Static / Dynamic logic circuit (register cell)
6. Latch
7. Pass transistor
8. Layout of any combinational circuit (complex CMOS logic gate).